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NOTICE OF ALLOWANCE AND FEE(S) DUE

21839

7590

02/26/2009

BUCHANAN, INGERSOLL & ROONEY PC POST OFFICE BOX 1404 ALEXANDRIA, VA 22313-1404 EXAMINER
SHECHTMAN, SEAN P
ART UNIT PAPER NUMBER
2121

DATE MAILED: 02/26/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/587,574	07/31/2006	Janne Kuivalainen	1034456-000049	8935

TITLE OF INVENTION: THERMAL OVERLOAD PROTECTION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	05/26/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

(571)-273-2885 or <u>Fax</u>

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for

maintenance fee notifications. Note: A certificate of mailing can only be used for domestic mailings of the CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission. 21839 02/26/2009 Certificate of Mailing or Transmission BUCHANAN, INGERSOLL & ROONEY PC I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below. POST OFFICE BOX 1404 ALEXANDRIA, VA 22313-1404 (Depositor's name (Signature (Date APPLICATION NO. FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. FILING DATE 10/587,574 07/31/2006 Janne Kuivalainen 1034456-000049 8935 TITLE OF INVENTION: THERMAL OVERLOAD PROTECTION APPLN. TYPE SMALL ENTITY ISSUE FEE DUE PUBLICATION FEE DUE PREV. PAID ISSUE FEE TOTAL FEE(S) DUE DATE DUE nonprovisional NO \$1510 \$300 \$0 \$1810 05/26/2009 **EXAMINER** ART UNIT CLASS-SUBCLASS SHECHTMAN, SEAN P 700-299000 1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). 2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. or agents OR, alternatively, (2) the name of a single firm (having as a member a ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required. registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type) PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY) 4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) 4a. The following fee(s) are submitted: lssue Fee A check is enclosed. Publication Fee (No small entity discount permitted) Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number ______ (enclose an extra copy of this fo Advance Order - # of Copies _ (enclose an extra copy of this form). 5. Change in Entity Status (from status indicated above) a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ■ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2). NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office. Authorized Signature Date Typed or printed name Registration No. This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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10/587,574	07/31/2006	07/31/2006 Janne Kuivalainen		8935	
21839 75	590 02/26/2009		EXAMINER		
BUCHANAN, IN	NGERSOLL & ROO	SHECHTMAN, SEAN P			
POST OFFICE BOX 1404			ART UNIT	PAPER NUMBER	
ALEXANDRIA, V	'A 22313-1404		2121		
			DATE MAILED: 02/26/2009		

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 0 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 0 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)				
	10/587,574 KUIVALAINEN ET AL.		AL.			
Notice of Allowability	Examiner	Art Unit				
	Sean P. Shechtman	2121				
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313 1. This communication is responsive to 2/5/09.	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS			
2. The allowed claim(s) is/are <u>1,3,6,7,11,14 and 19</u> .						
 3. Acknowledgment is made of a claim for foreign priority unapprint and all blooms are less than all blooms and all blooms are less than all blooms and all blooms are less than all blooms. 	been received. been received in Application No		tion from the			
* Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" on noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the rec	quirements			
4. A SUBSTITUTE OATH OR DECLARATION must be subminification (PTO-152) which give			OTICE OF			
. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.						
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached						
1) hereto or 2) to Paper No./Mail Date	1) hereto or 2) to Paper No./Mail Date					
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date						
ldentifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).						
 DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT I 			Note the			
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5.	atent Application				
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary	(PTO-413),				
3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date	Paper No./Mail Dat 7.	nent/Comment				
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8.	ent of Reasons for Allo	wance			

Application/Control Number:

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DETAILED ACTION

Claim Rejections – 35 USC § 112

1. Rejections withdrawn.

Claim Rejections - 35 USC § 103

2. Rejections withdrawn.

Allowable Subject Matter

3. Claims 1, 3, 6, 7, 11, 14, 19, are allowed.

The following is an examiner's statement of reasons for allowance:

While Libert teaches device/method/processor and memory storing instructions for thermal overload protection of an electrical device, particularly an electric motor (Abstract; Col. 1, lines 46-64), comprising a current meter configured to measure at least one load current supplied to the electrical device (Col. 33, lines 50-51), a processor system configured to calculate a thermal load on the electrical device on the basis of said at least one load current, and a switch device disconnecting a current supply when the thermal load reaches a given threshold level, said processor system employing X-bit, and being configured to scale the measured current into unit values to a range of 0 to Y, wherein Y represents Y/100% of a nominal current and Y is a real number greater than zero (Col. 11, lines 24-56), and to calculate the thermal load using a mathematical equation that, together with its operands, is programmed into the processor system structured (Col. 33, line 52 – Col. 34, line 5; Col. 22, lines 29 – Col. 23, line 49).

And, Chan teaches a processor system employing 32-bit fixed-point arithmetic, and the processor system structured such that calculating a result or a provisional result never exceeds the 32-bit value (paragraph 30 – paragraph 58, especially paragraph 51). It is inherent that fixed-point number can exactly represent any integer only within the range determined by the magnitude bits, 32 in the case of the 32-bit DSP.

And, Onaga teaches a processor system employing X-bit fixed-point arithmetic, and the processor system structured such that a result or a provisional result never exceeds the X-bit value (Col. 11, lines 1-5, TMS-32010 16bit processor). It is inherent that fixed-point number can exactly represent any integer only within the range determined by the magnitude bits, 16 in the case of the TMS-32010.

And, Moscrip teaches a processor system employing 32-bit fixed-point arithmetic, structured such that a result or a provisional result never exceeds the 32-bit value (Col. 13, lines 36- Col. 14, line 9). It is inherent that fixed-point number can exactly represent any integer only within the range determined by the magnitude bits, 32 in the case of the MC68332 or 32-bit fixed point DSPs.

Referring to claims 1, 7, 19, none of Libert, Chan, Onaga or Moscrip, taken either alone or in obvious combination discloses a method/device/processor and memory storing executable instructions having all the claimed features of applicant's instant invention, specifically including:

calculating the thermal load using a mathematical equation structured such that a result or a provisional result never exceeds the 32-bit value;

wherein the mathematical equation is .THETA. k = .DELTA. .times. .times. T * i 2 C + (1 - .DELTA. .times. .times. T R * C) * .THETA. k - 1;

wherein .THETA.=currently calculated thermal load, wherein .THETA. k-1=previous thermal load, .DELTA.T=interval for thermal load calculation, R=cooling factor of electrical device, C=trip-class factor, i= measured current.

Claims 3, 6, 11, 14 depend from claims 1, 7, or 19 and are therefore also allowable. It is for these reasons that applicant's invention defines over the prior art of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. The prior art or art made of record and not relied upon is considered pertinent to applicant's disclosure.

The following patents or publications are cited to further show the state of the art with respect to a device and method for thermal overload protection of an electrical device, particularly an electric motor (M), the device comprising means for measuring at least one load current supplied to the electrical device (M), means for calculating the thermal load on the electrical device on the basis of said at least one load current, and means (S2) for interrupting a current supply (L1, L2, L3) when the thermal load reaches a given threshold level, wherein the device comprises a processor system employing X-

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bit, preferably X=32, fixed-point arithmetic, the system comprising means for scaling the current measured into unit values to a range of 0 to Y, wherein Y represents Y/100% of a nominal current, and means for calculating a time-to-trip in accordance with formula, whereby the equation and its operands are programmed into the microprocessor system structured such that a result or a provisional result never exceeds the X-bit value.

U.S. Pub. No. 2008/0253041 to Kuivalainen.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sean P. Shechtman whose telephone number is (571)272-3754. The examiner can normally be reached on 9:30am-6:00pm, M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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SPS Sean P. Shechtman February 14th, 2009

/Sean P. Shechtman/ Primary Examiner, Art Unit 2121